

ULTRA LOW CAPACITANCE STEERING DIODE/TVS ARRAY



DESCRIPTION

The PLR0508is an ultra low capacitance steering diode/TVS array. This device is designed to protect computing applications such as gigabit Ethernet, HDMI, USB and DVI interfaces as well as telecommunication equipment and systems. The PLR0508 is available in the space-saving DFN-10 package configuration and is rated at 200 Watts peak pulse current (8/20µs waveshape).

This device meets the IEC 61000-4-2 (ESD), 61000-4-2 (EFT) and 61000-4-4 (Surge) requirements. At higher operating frequencies or faster edge rates, insertion loss and signal integrity are a major concern. This device in conjunction with passive components integrated into a TVS/filter network can be used for EMI/RFI protection.

FEATURES

- Compatible with IEC 61000-4-2 (ESD): Air 15kV, Contact 8kV
- Compatible with IEC 61000-4-4 (EFT): 40A 5/50ns
- 200 Watts Peak Pulse Power per Line(tp = 8/20μs)
- ESD Protection > 25 kilovolts
- · Low Clamping Voltage
- Unidirectional Configuration
- Protects 8 I/O Ports & Power Supply or 8 Lines
- Ultra Low Capacitance: 1.6pF (I/O to I/O)
- RoHS Compliant
- REACH Compliant

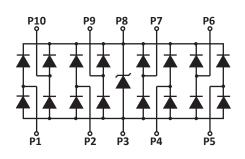
MECHANICAL CHARACTERISTICS

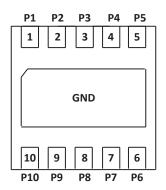
- Molded JEDEC DFN-10 Package
- Approximate Weight: 7 milligrams
- Lead-Free Pure-Tin Plating (Annealed)
- Solder Reflow Temperature:
 - Pure-Tin Sn, 100: 260-270°C
- 8mm Tape and Reel Per EIA Standard 481
 Flammability Rating UL 94V-0

APPLICATIONS

- Gigabit Ethernet
- DVI, USB and HDMI Interfaces
- High-Speed Data Line ESD Protection
- FireWire, SATA & PCIe Interfaces

CIRCUIT DIAGRAM & PIN CONFIGURATION





TYPICAL DEVICE CHARACTERISTICS

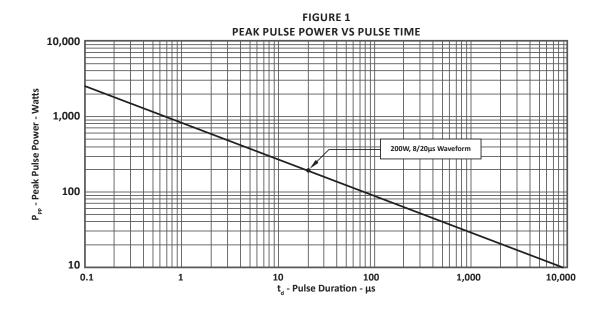
MAXIMUM RATINGS @ 25°C Unless Otherwise Specified								
PARAMETER SYMBOL VALUE								
Peak Pulse Power (tp = 8/20μs) - See Figure 1	P _{PP}	200	Watts					
Operating Temperature	T _L	-55 to 150	°C					
Storage Temperature	T _{stg}	-55 to 150	°C					

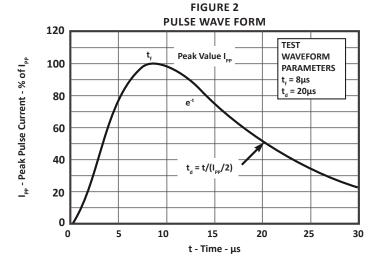
ELECTRICAL CHARACTERISTICS PER LINE @ 25°C Unless Otherwise Specified									
PART NUMBER	DEVICE MARKING								
		V _{wM} VOLTS	@ 1mA V _(BR) VOLTS	@ I _p = 1A V _c VOLTS	@ I _p = 5A V _c VOLTS	@V _{wм} Ι _D μΑ	@0V, 1MHz C _{J(SD)} pF		
PLR0508	508	5.0	6.0	9.8	13.0	1.0	1.6		

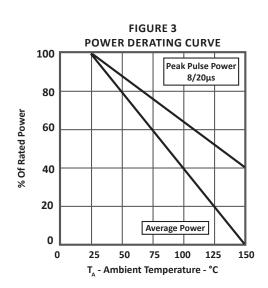
NOTE

- From I/O Pin to I/O.
 Across TVS only (Pin 3 to 8).

TYPICAL DEVICE CHARACTERISTICS

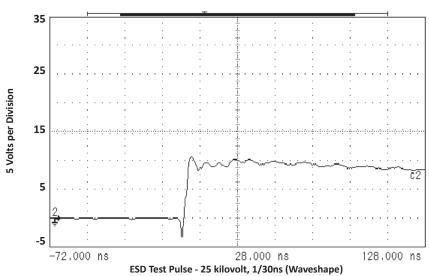






TYPICAL DEVICE CHARACTERISTICS

FIGURE 4
OVERSHOOT & CLAMPING VOLTAGE





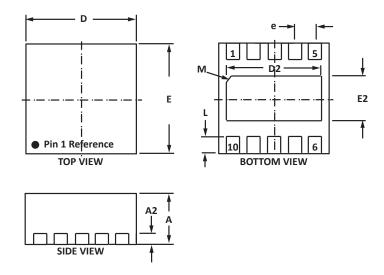


DFN-10 PACKAGE INFORMATION

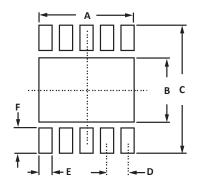
OUTLINE DIMENSIONS								
DIM	MILLIN	IETERS	INCHES					
	MIN	MAX	MIN	MAX				
А	0.45	0.55	0.017	0.021				
A2	0.13	BSC	0.005	5 BSC				
D	2.50	2.70	0.097	0.105				
D2	2.10	2.20	0.083	0.085				
E	2.50	2.70	0.097	0.105				
E2	1.21	1.31	0.046	0.051				
е	0.50	BSC	0.020) BSC				
L	0.35	0.45	0.013	0.017				
М	0.25	0.45	0.010	0.018				

NOTES

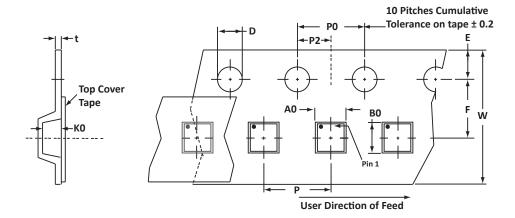
- 1. Controlling dimension: millimeters.
- 2. Dimensioning and tolerances per ANSI Y14.M, 1985.
- 3. Coplanarity applies to the exposed pad as well as the terminals.



PAD LAYOUT DIMENSIONS						
DIM	MILLIMETERS	INCHES				
DIM	NOMINAL	NOMINAL				
А	2.25	0.089				
В	B 1.42 0.056					
С	2.90	0.114				
D	0.50 BSC	0.020 BSC				
E	E 0.30 0.012					
F	F 0.58 0.023					
NOTES 1. Controlling dimension: millimeters.						



TAPE AND REEL



SPECIFICATIONS												
REEL DIA.	TAPE WIDTH	A0	В0	ко	D	E	F	w	P0	P2	Р	tmax
178mm (7")	8mm	2.90 ± 0.10	2.90 ± 0.10	0.80 ± 0.10	1.50 ± 0.10	1.75 ± 0.10	3.50 ± 0.05	8.00 ± 0.30	4.00 ± 0.10	2.00 ± 0.05	4.00 ± 0.10	0.25

NOTES

- 1. Dimensions are in millimeters.
- 2. Surface mount product is taped and reeled in accordance with EIA-481.
- 3. Suffix T73 = 7" Reel 3,000 pieces per 8mm tape.
- 4. Marking on Part marking code (see page 2) and polarity dot.

Package outline, pad layout and tape specifications per document number 06080.R0 3/11.

ORDERING INFORMATION							
BASE PART NUMBER	ASE PART NUMBER LEADFREE SUFFIX TAPE SUFFIX QTY/F				TUBE QTY		
PLR0508	-LF	-Т73	3,000	7"	n/a		

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COMPANY INFORMATION

COMPANY PROFILE

ProTek Devices, based in Tempe, Arizona USA, is a manufacturer of Transient Voltage Suppression (TVS) products designed specifically for the protection of electronic systems from the effects of lightning, Electrostatic Discharge (ESD), Nuclear Electromagnetic Pulse (NEMP), inductive switching and EMI/RFI. With over 25 years of engineering and manufacturing experience, ProTek designs TVS devices that provide application specific protection solutions for all electronic equipment/systems.

ProTek Devices Analog Products Division, also manufactures analog interface, control, RF and power management products.

CONTACT US

Corporate Headquarters

2929 South Fair Lane Tempe, Arizona 85282 USA

By Telephone

General: 602-431-8101 Sales: 602-414-5109

Customer Service: 602-414-5114

By Fax

General: 602-431-2288

By E-mail:

Sales: sales@protekdevices.com

Customer Service: service@protekdevices.com
Technical Support: support@protekdevices.com

Web

www.protekdevices.com www.protekanalog.com

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